



#CS_IO_A..... FPGA Pin
#B_RD..... FPGA Pin
#WE_Par..... FPGA Pin

LA1 FPGA Pin
LA1 FPGA Pin
LA1 FPGA Pin

D0 FPGA Pin
D1 FPGA Pin
D2 FPGA Pin
D3 FPGA Pin
D4 FPGA Pin
D5 FPGA Pin
D6 FPGA Pin
D7 FPGA Pin